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Behavioural Model of Integrated Circuits for EMC Prediction Franco Fiori Eln. Dept., Politecnico di Torino Corso Duca degli Abruzzi, 24, Torino, Italy franco.fiori@polito.it Abstract This paper deals with the modeling of integrated circuits for electromagnetic compatibility (EMC) analysis. With reference to complex integrated circuits that usually include huge core logic blocks, analog and

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EMC-aware analog integrated circuit design is domain which is continuously gaining in importance; Paradoxically, literature describing and solving EMC problems in integrated circuits is not widely spread: most EMC-related works describe what should be done outside the integrated circuit (like shielding, etc).

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