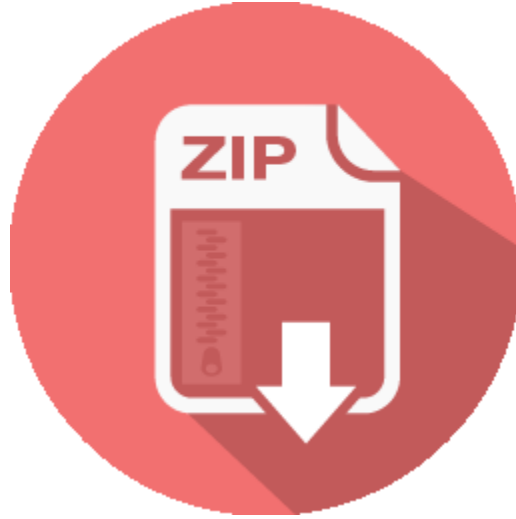


# POWER MANAGEMENT OF DIGITAL CIRCUITS IN DEEP SUBMICRON CMOS TECHNOLOGIES%0A



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