

[TUNNELING FIELD EFFECT TRANSISTOR TECHNOLOGY%0A](#)



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Tunneling Field Effect Transistors Robert B Laughlin

In field effect transistors, tunnelling occurs with barriers of thickness around 1-3 nm and smaller in which the gate is controlled via quantum tunnelling rather than by thermal injection, reducing gate voltage from 1 volt to 0.2 volts and reducing power consumption by up to 100x. If these transistors can be scaled up into VLSI chips, they will significantly improve the performance per

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The Tunnel Field Effect Transistor (TFET) is becoming the most promising device to respond to the demanding requirements of future technology requirements. The benefits of the TFET are especially linked to its potential for sub-60mV/decade sub-threshold swings, a pre-requisite for scaling the supply voltage well below 1V.

Furthermore, the TFET has reduced short-channel effects compared to the

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